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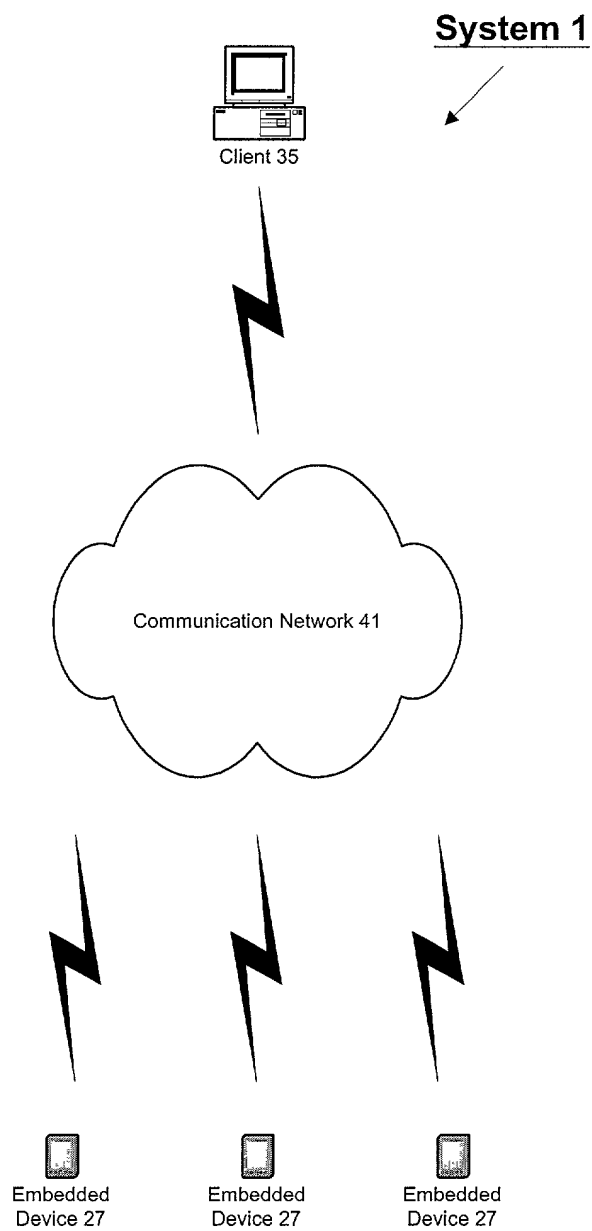


Fig. 1

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Embedded
Device 27
Software

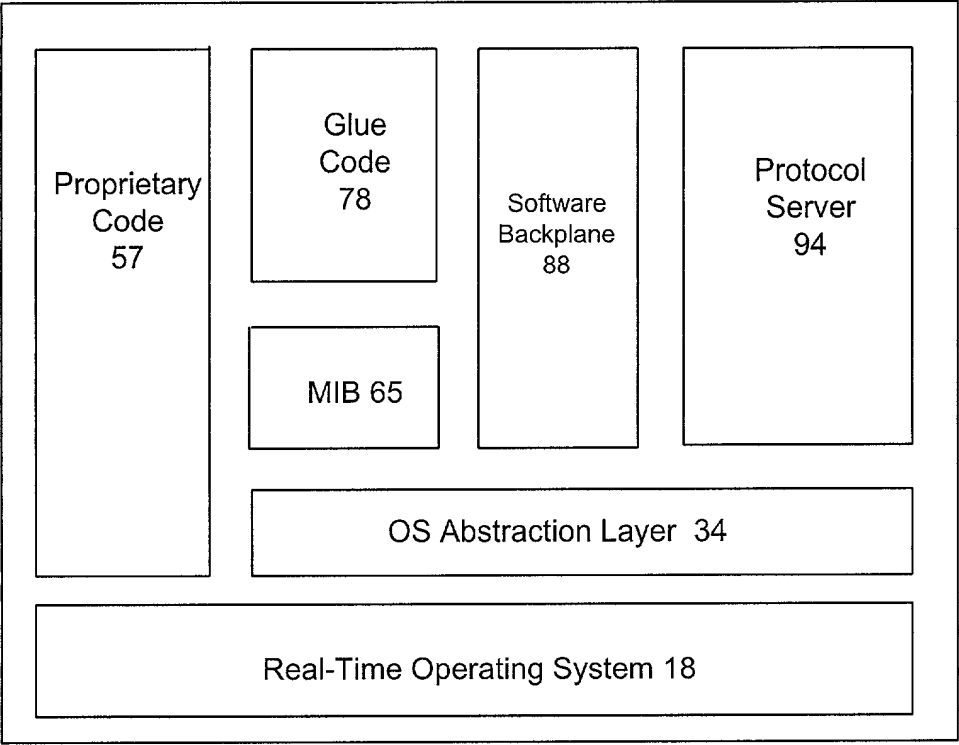


Fig. 2

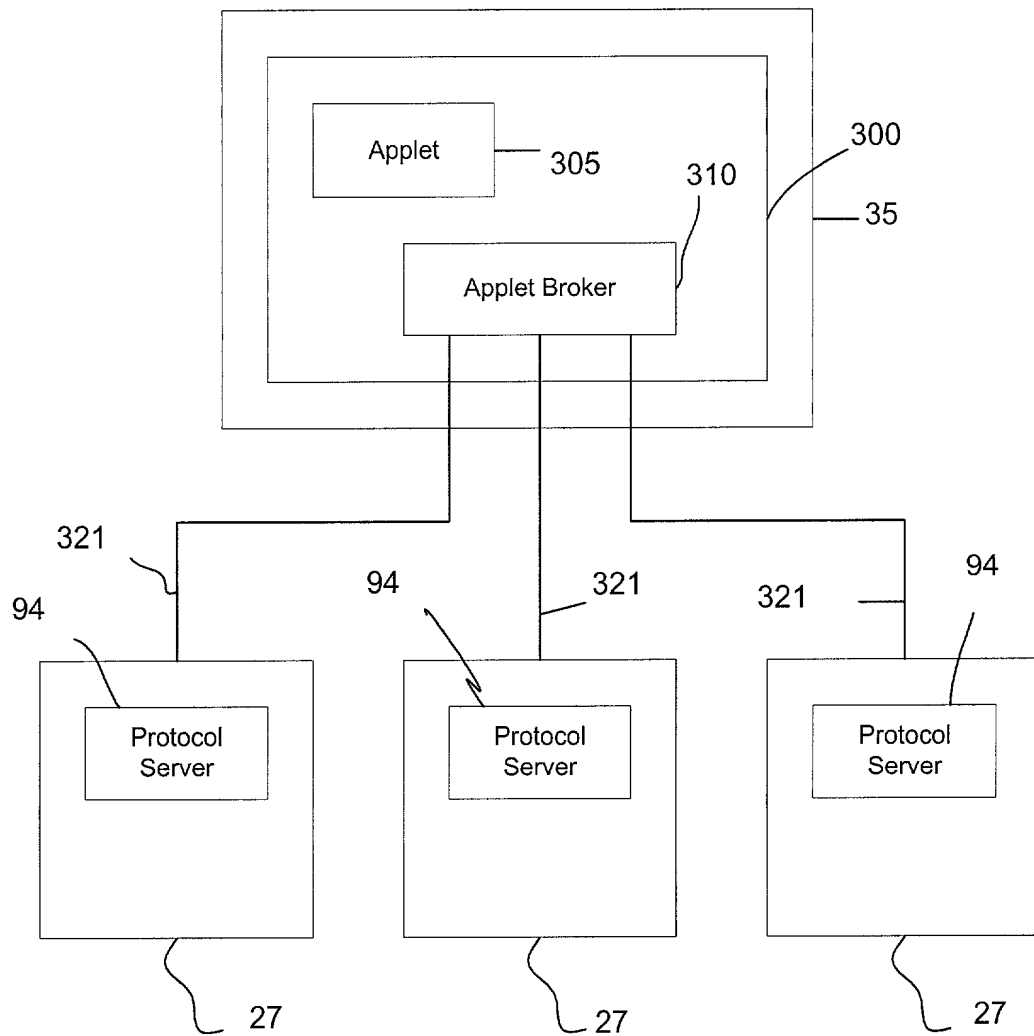


Fig. 3

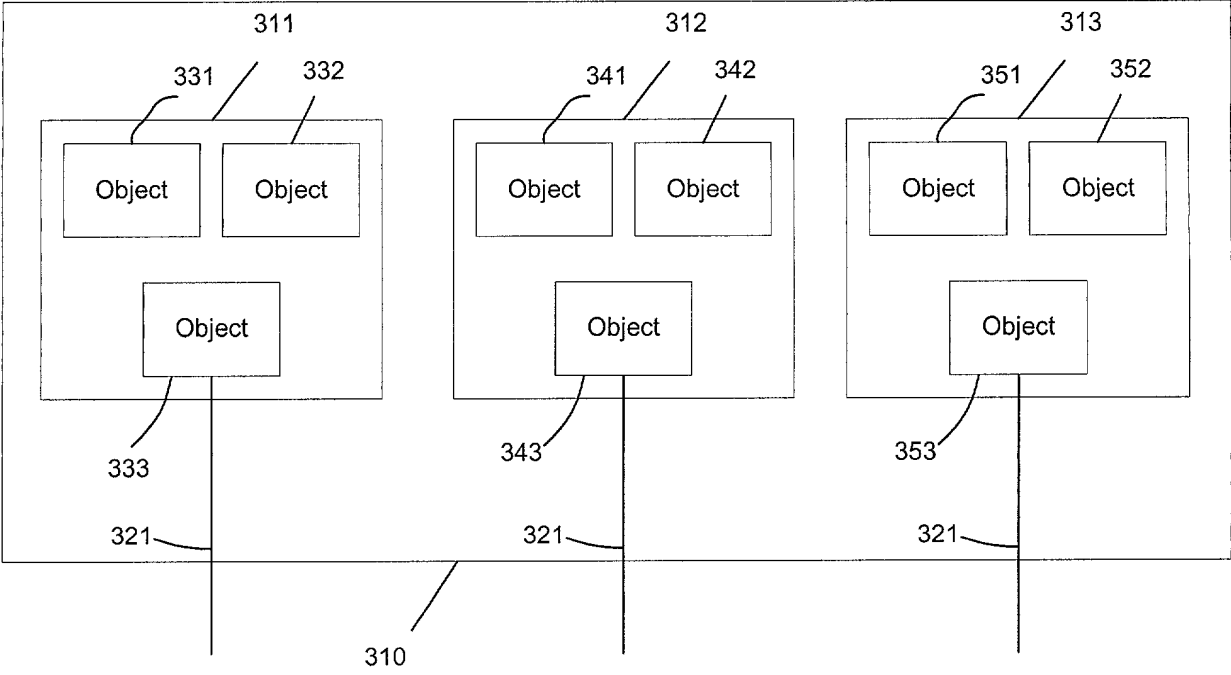


Fig. 4

Header	Option Chunks	Data Chunks	Zero Chunk	CRC Checksum
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Fig. 5

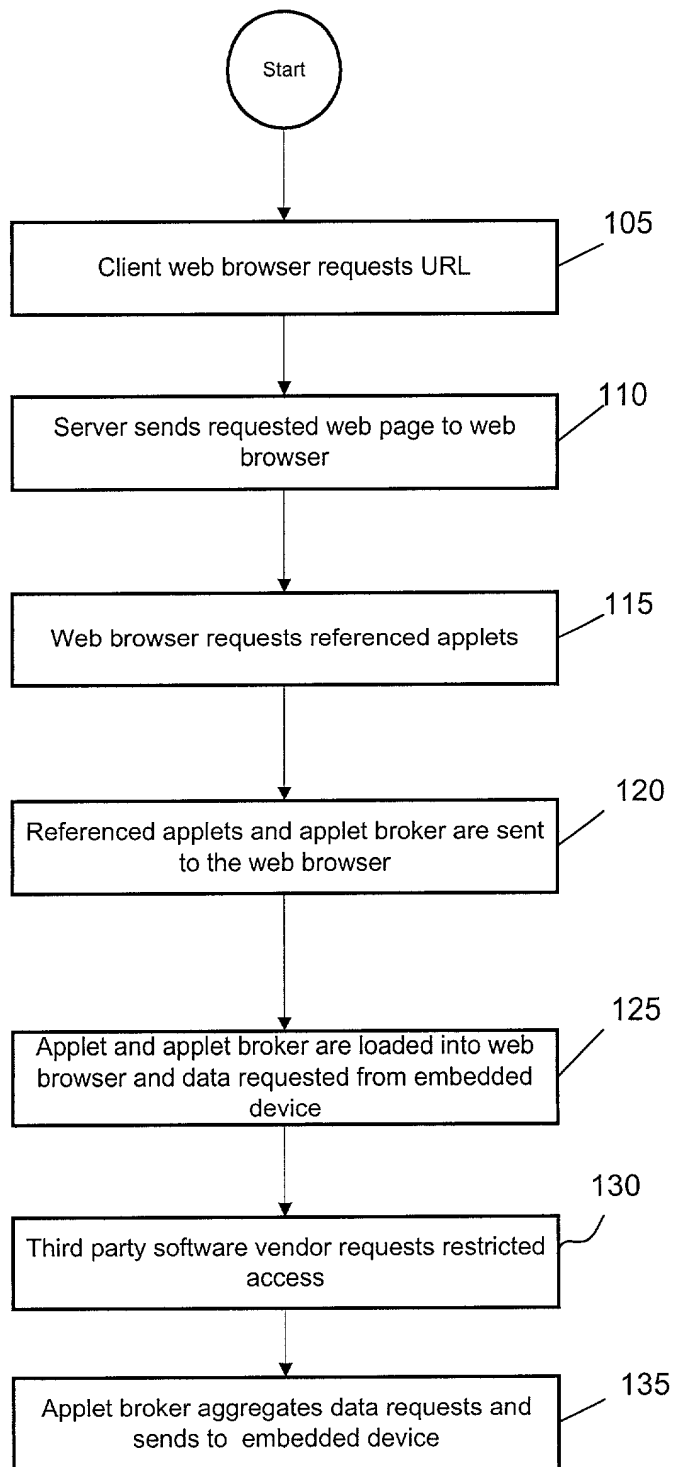


Fig. 6

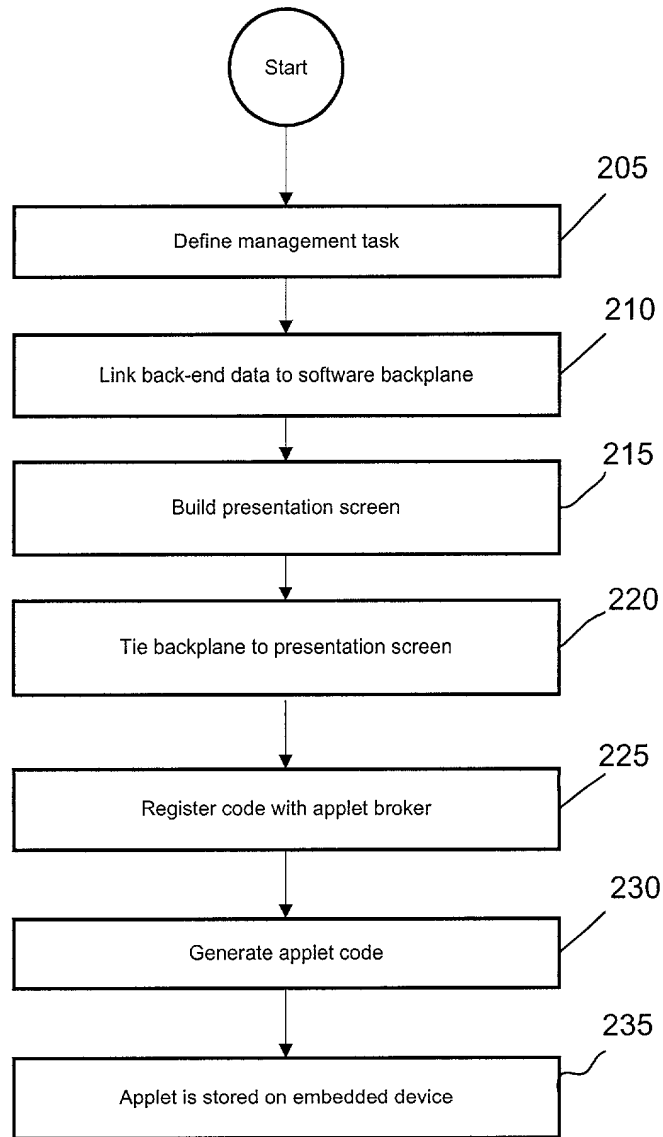


Fig. 7

FIG. 8 is a block diagram of a system architecture. The system 27 includes a Real-Time Operating System 18, an OS Abstraction Layer 34, and several components: Proprietary Code 57, Glue Code 78, MIBway 65, Software Backplane 88, and Applet Server 94. A detailed view of the Applet Server 94 is shown in an inset, revealing it contains a Pie Chart Applet 306 and an Applet Broker 310. The Pie Chart Applet 306 includes a Pie Chart Bean 305 and a circle 300, with an arrow pointing from the circle to the Applet Broker 310.

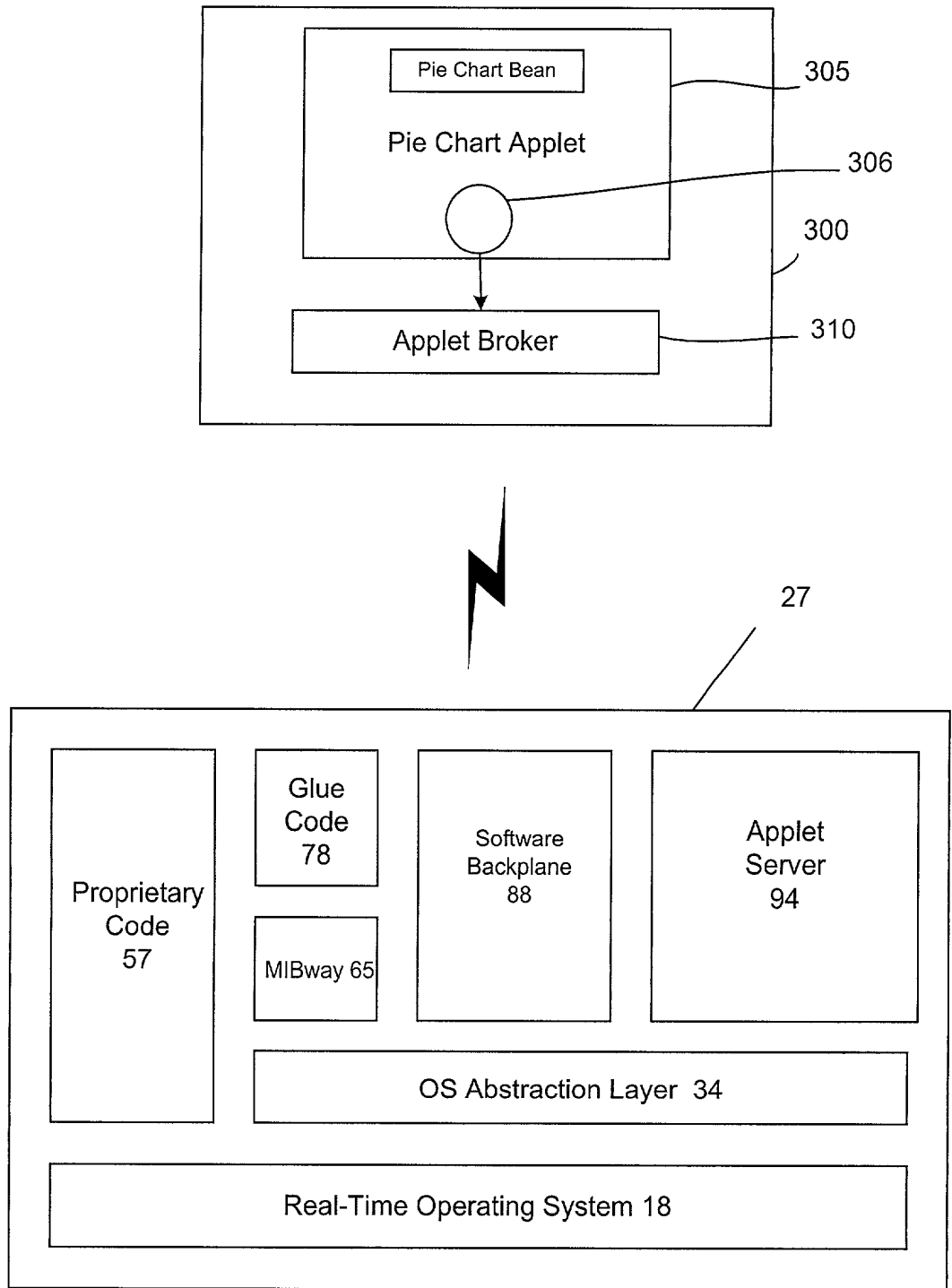


Fig. 8